

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	0	((((plurality multiplicity) with (memory DIMM SIMM) with (memory near2 card)) and ((memory near2 card) same ((memory near2 controller) controller (control near2 circuit)) same JTAG)).clm.	US-PGPUB	OR	ON	2007/07/18 11:30

Interference Search History Printout

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	7	((plurality multiplicity) with (memory DIMM SIMM) with (memory near2 card)) and ((memory near2 card) same ((memory near2 controller) controller (control near2 circuit)) same JTAG)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/18 11:30
L4	0	((((plurality multiplicity) with (memory DIMM SIMM) with (memory near2 card)) and ((memory near2 card) same ((memory near2 controller) controller (control near2 circuit)) same JTAG)).clm.	US-PGPUB	OR	ON	2007/07/18 11:30
L3	0	((((plurality multiplicity) with (memory DIMM SIMM) with (memory near2 card)) and ((memory near2 card) same ((memory near2 controller) controller (control near2 circuit)) same slots same JTAG)).clm.	US-PGPUB	OR	ON	2007/07/18 11:30
L2	0	((plurality multiplicity) with (memory DIMM SIMM) with (memory near2 card)) and ((memory near2 card) same ((memory near2 controller) controller (control near2 circuit)) same slots same JTAG)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/18 11:30
L1	0	((plurality multiplicity) near3 (memory DIMM SIMM) with (memory near2 card)) and ((memory near2 card) same ((memory near2 controller) controller (control near2 circuit)) same slots same JTAG)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/18 11:28
S50	16	(memory DIMM SIMM) and ((memory near2 card) same ((memory near2 controller) controller (control near2 circuit)) same JTAG)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/18 11:24
S49	1	(test\$3 near5 (memory DIMM SIMM)) and ((test\$3 near5 (memory near2 card)) same ((memory near2 controller) controller (control near2 circuit)) same JTAG)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/18 09:43

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S19	83	(test\$3 near5 (memory DIMM SIMM)) same (test\$3 near5 (memory near2 card)) same ((memory near2 controller) controller (control near2 circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/17 16:39
S48	38	S47 and ((memory with module) same buffer same test\$3 and card)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/27 11:52
S25	560	(memory with module) same buffer same test\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/27 11:52
S47	15434	(714/30-32,40-44,718,719,723,726, 727 365/200,201).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/27 11:51
S46	1	10/660006	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/26 16:58
S45	0	10/660006	USPAT; USOCR	OR	ON	2007/03/26 16:58
S44	0	"20050060604"	USPAT; USOCR	OR	ON	2007/03/26 16:58
S39	15	S36 and processor with (JTAG "1149") with bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/26 16:58
S43	22	S38 not S39	USPAT; USOCR	OR	ON	2007/03/26 16:57
S42	1	"6704895".PN.	USPAT; USOCR	OR	ON	2007/03/26 16:35
S41	1	"6954879".PN.	USPAT; USOCR	OR	ON	2007/03/26 15:39
S40	1	"5331571".PN.	USPAT; USOCR	OR	ON	2007/03/26 15:38

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S38	52	S36 and processor with (JTAG "1149")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/26 15:33
S37	1480	S36 processor with (JTAG "1149")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/26 15:32
S36	188	test\$3.clm. and memory.clm. and ((module card dimm simm) with (jtag tap "1149"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/26 15:32
S35	3	S34 and memory adj card	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/26 15:29
S34	92	(memory with module) same buffer and service adj processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/26 13:59
S24	11175	(memory with module) same buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/26 13:59
S33	10	((test\$3 with memory with module) same (test\$3 near5 controller) same (test\$3 with memory near2 card))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/26 13:43
S32	39	((test\$3 with memory with module) and (test\$3 near5 controller) and (test\$3 with memory near2 card))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/26 13:42

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S31	4	(memory with module) same buffer same test\$3 and (memory near2 card) and ((isolat\$3 identify\$3) with (defective faulty bad) with (module component))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/26 13:40
S26	69	(memory with module) same buffer same test\$3 and (memory near2 card)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/26 13:37
S30	1	"20020186031".PN.	US-PGPUB	OR	ON	2007/03/23 15:25
S29	4	("6658516" "6643725").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 15:17
S28	62	S26 not S27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 15:16
S27	7	S26 and (test\$3 with (separately individually singly))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 15:03
S13	7	S12 and (test\$3 near3 (separately individually singly))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 15:02
S23	35943	(memory with module) and buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 15:01
S22	75	S19 not S14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 15:00

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S21	3	S20 not S13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 13:02
S20	6	S19 and (test\$3 near3 (separately individually singly))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 13:02
S18	321	(test\$3 near5 (memory DIMM SIMM)) and (test\$3 near5 (memory near2 card)) and ((memory near2 controller) controller (control near2 circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 13:01
S14	15	(test\$3 with (memory adj module DIMM SIMM)) and (test\$3 with (memory near2 card)) and (test\$3 with (memory near2 controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 12:59
S17	2	"4736374".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 12:46
S16	2	"20020199142"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 12:46
S15	2	("5357521").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 11:05
S12	308	S11 and (memory near2 card) same (memory near2 controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 10:43

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S11	32690	((isolat\$3 detect\$3 find\$3 test\$3) with (fault error mismatch defect) with (DIMM memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 10:40
S9	424	S7 and (memory near2 card) same (memory near2 controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 10:17
S10	32674	((isolat\$3 detect\$3 find\$3 test\$3) with (fault error mismatch defect) with memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 10:05
S7	129362	((isolat\$3 detect\$3 find\$3 test\$3) with (fault error mismatch defect) with (component circuit memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 10:04
S8	1037	S7 and (memory near2 card) and (memory near2 controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 09:53
S6	32674	((isolat\$3 detect\$3 find\$3 test\$3) with (fault error mismatch defect) with memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 09:47
S3	9	(isolat\$3 with defect with memory) and ("International Business Machines Corporation").as.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 09:42
S5	5	S3 not S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 15:49

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S4	4	(isolat\$3 with defect with memory). clm. and ("International Business Machines Corporation").as.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 15:47
S2	2	"20050060604"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 15:43